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| Application No. | 10/661,738 |
| Filing Date | September 11, 2003 |
| First Named Inventor | Peter Tolchinsky |
| Art Unit | |
| Examiner Name | Luan C. Thai |
| Total Number of Pages in This Submission | 9 |
| Attorney Docket Number | 42P17299 |

ENCLOSURES (check all that apply)

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| Remarks | | |

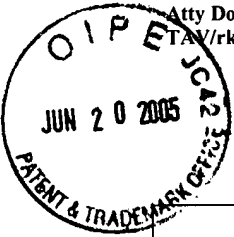
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| Firm or Individual name | Tom Van Zandt, Reg. No. 43,219 BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP |
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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| In Re Patent Application of: Peter Tolchinsky et al. Application No.: 10/661,738 Filed: September 11, 2003 For: METHODS AND APPARATUSES FRO MANUFACTURING ULTRA THIN DEVICE LAYERS FOR INTEGRATED CIRCUIT DEVICES | Examiner: Luan C. Thai Art Unit: 2891 Confirmation No.: 8287 |
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AMENDMENT AND RESPONSE TO OFFICE ACTION


Sir:

In response to the Office Action mailed June 6, 2005, please enter the following amendment.

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